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<td>Table 2-14</td>
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<tr>
<td>Table 3-3</td>
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</tr>
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</tr>
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<td>26</td>
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<tr>
<td>Table 5-5</td>
<td>Device Status Scenario Feedback</td>
<td>27</td>
</tr>
<tr>
<td>Table 6-1</td>
<td>U.S. FDA Accession Number</td>
<td>29</td>
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## Revision History

<table>
<thead>
<tr>
<th>Revision</th>
<th>Description</th>
<th>Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>001</td>
<td>Initial Public Release</td>
<td>January 2021</td>
</tr>
</tbody>
</table>
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         | Table 4-2: Face Authentication Metrics  
         | Table 4-3: Performance Metrics  
         | Figure 7-1. Intel® RealSense™ ID Solution F455  
         | Figure 7-2. Intel® RealSense™ ID Solution F450 | April 2021 |
| 003      | Table 1-1: Intel® RealSense™ ID Solution F450/F455  
         | Table 2-1: Intel® RealSense™ ID Solution F450 Component List | May 2021 |
| 004      | Updated sections 2.4 and 2.5 for updating firmware  
         | Updated sections 4.1, 4.2, 4.3  
         | Updated Table 4.1, functional metrics | February 2024 |
| 005      | Minor corrections and edits | March 2024 |
The Intel® RealSense™ ID Solution F450 is an embedded face authentication module built upon a closed real-time operating system (RTOS) using neural network algorithms. The Intel® RealSense™ ID Solution F450 module integrates the optical components and processing hardware necessary to integrate high performance, local face authentication capabilities into new and existing product types. The Intel® RealSense™ ID Solution F455 is a peripheral device that incorporates the Intel® RealSense™ ID Solution F450 module.

Table 1-1: Intel® RealSense™ ID Solution F450/F455

<table>
<thead>
<tr>
<th>CONFIG</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>Module</td>
<td>The Intel® RealSense™ ID Solution F450 is an embedded device. An SOC controls all boot-up and runtime flows of the Intel® RealSense™ ID Solution F450 module through an internal data link connection.</td>
</tr>
<tr>
<td>Peripheral</td>
<td>The Intel® RealSense™ ID Solution F450 integrated into a mechanical housing. The Intel® RealSense™ ID Solution F455 connects to an external system through an external cable, typically USB2.</td>
</tr>
</tbody>
</table>
Figure 1-2: Intel® RealSense™ ID Solution F450 Module System Topology
2 Hardware Details

The Intel® RealSense™ ID Solution F450 module is a single optical PCB subassembly with processing capabilities that the product will mount into a system chassis and connect to a host. The optical components consist of both image sensors and illumination components.

Figure 2-1: Intel® RealSense™ ID Solution F450 Module Front Side

![Figure 2-1: Intel® RealSense™ ID Solution F450 Module Front Side](image)

Table 2-1: Intel® RealSense™ ID Solution F450 Component List

<table>
<thead>
<tr>
<th>SECTION</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>SOC</td>
<td>System on Chip</td>
</tr>
<tr>
<td>Right Camera</td>
<td>Full HD Camera</td>
</tr>
<tr>
<td>Left Camera</td>
<td>Full HD Camera</td>
</tr>
<tr>
<td>Projector</td>
<td>850nm IR</td>
</tr>
<tr>
<td>LED</td>
<td>850nm IR</td>
</tr>
<tr>
<td>Host Connector</td>
<td>IPEX 20708-050E Plug (50pin)</td>
</tr>
<tr>
<td>Crystal</td>
<td>24MHz</td>
</tr>
<tr>
<td>Flash</td>
<td>512Mb Storage</td>
</tr>
</tbody>
</table>

Figure 2-2: Intel® RealSense™ ID Solution F450 Module Rear Side

![Figure 2-2: Intel® RealSense™ ID Solution F450 Module Rear Side](image)
2.1 Host Interconnect

The Intel® RealSense™ ID Solution F450 module requires external power and a serial data link for initialization and control of the Face Authentication system. Data signals and external power are both routed through the same host connector interface.

2.1.1 Data Interface

The Intel® RealSense™ ID Solution F450 supports both UART and USB2 interfaces for the data link.

<table>
<thead>
<tr>
<th>SECTION</th>
<th>DATA RATE</th>
<th>PINS</th>
</tr>
</thead>
<tbody>
<tr>
<td>UART (3V Interface)</td>
<td>115.2kbs to 4Mbps</td>
<td>UART_TX (Pin 47), UART_RX (Pin 49)</td>
</tr>
<tr>
<td>USB2</td>
<td>480Mbps</td>
<td>USB2_DP (Pin 46), USB2_DN (Pin 48)</td>
</tr>
</tbody>
</table>

2.1.2 Power Interface

The Intel® RealSense™ ID Solution F450 module has a peak power of up to 3.6W. The host system must ensure the power supply can satisfy this peak power based on the chosen Intel® RealSense™ ID Solution F450 voltage source (VDD). If the host system supports power gate control it is the responsibility of the host system to ensure no conflicts between power sequencing and data link initialization occurs during power-up and power-down cycles.

<table>
<thead>
<tr>
<th>PIN</th>
<th>VOLTAGE RANGE</th>
<th>PEAK POWER</th>
</tr>
</thead>
<tbody>
<tr>
<td>VDD (Pin 51/52)</td>
<td>3.15 to 5.0V</td>
<td>2.1 – 3.6W(1)(2)</td>
</tr>
<tr>
<td>GND</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

**NOTES:**
(1) The power range is primarily dependent on illumination power set by the device.
(2) The typical max current for a USB2 port is 450mA, USB3 port is 900mA.

The Intel® RealSense™ ID Solution F450 supports three device states, off, standby, and operation. In the off state the Intel® RealSense™ ID Solution F450 is shut down and the power supply from the host is disabled. In the standby, the Intel® RealSense™ ID Solution F450 power supply is enabled, the SOC is in an execution loop for manageability access or FW update, but the optical components are powered down. In the operation state, the power supply is enabled, SOC is running FA enrollment or authentication usage with the optical components activated.
### Table 2-4: Power Consumption

<table>
<thead>
<tr>
<th>SECTION</th>
<th>POWER CONSUMPTION</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Off</td>
<td>0</td>
<td>mW</td>
</tr>
<tr>
<td>Operation</td>
<td>1.3 – 1.5</td>
<td>W</td>
</tr>
</tbody>
</table>

### 2.1.3 Receptacle Pin Out

The connector plug part number is IPEX 20708-050E. The required host cable plug for connecting the Intel® RealSense™ ID Solution F450 module must use IPEX 20709-050E receptacle.

### Table 2-5: Intel® RealSense™ ID Solution F450 Module Pin Out

<table>
<thead>
<tr>
<th>PIN</th>
<th>SIGNAL</th>
<th>PIN</th>
<th>SIGNAL</th>
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</thead>
<tbody>
<tr>
<td>2</td>
<td>GND</td>
<td>1</td>
<td>CPU_RXD</td>
</tr>
<tr>
<td>4</td>
<td>AUDGPIO_2</td>
<td>3</td>
<td>CPU_TXD</td>
</tr>
<tr>
<td>6</td>
<td>AUDGPIO_1</td>
<td>5</td>
<td>GND</td>
</tr>
<tr>
<td>8</td>
<td>GND</td>
<td>7</td>
<td>-</td>
</tr>
<tr>
<td>10</td>
<td>CSI_DNI</td>
<td>9</td>
<td>I2C_SDA18</td>
</tr>
<tr>
<td>12</td>
<td>CSI_DPI</td>
<td>11</td>
<td>I2C_SCL18</td>
</tr>
<tr>
<td>14</td>
<td>GND</td>
<td>13</td>
<td>HOST_ADC_In</td>
</tr>
<tr>
<td>16</td>
<td>AUDGPIO_0</td>
<td>15</td>
<td>EXT_RSTn (Active Low)</td>
</tr>
<tr>
<td>18</td>
<td>-</td>
<td>17</td>
<td>GPO_1</td>
</tr>
<tr>
<td>20</td>
<td>GND</td>
<td>19</td>
<td>GPO_0</td>
</tr>
<tr>
<td>22</td>
<td>GPIO_6</td>
<td>21</td>
<td>GND</td>
</tr>
<tr>
<td>24</td>
<td>GPIO_5</td>
<td>23</td>
<td>-</td>
</tr>
<tr>
<td>26</td>
<td>GND</td>
<td>25</td>
<td>SDO1</td>
</tr>
<tr>
<td>28</td>
<td>GPIO_3</td>
<td>27</td>
<td>SCK1</td>
</tr>
<tr>
<td>30</td>
<td>GPIO_2</td>
<td>29</td>
<td>SDI1</td>
</tr>
<tr>
<td>32</td>
<td>GND</td>
<td>31</td>
<td>SEN1</td>
</tr>
<tr>
<td>34</td>
<td>CSI_CKN</td>
<td>33</td>
<td>GPO_4</td>
</tr>
<tr>
<td>36</td>
<td>CSI_CKP</td>
<td>35</td>
<td>GND</td>
</tr>
<tr>
<td>38</td>
<td>GND</td>
<td>37</td>
<td>CSI_VSD0</td>
</tr>
<tr>
<td>40</td>
<td>CSI_DNO</td>
<td>39</td>
<td>SCL_3</td>
</tr>
<tr>
<td>42</td>
<td>CSI_DPO</td>
<td>41</td>
<td>SDA_3</td>
</tr>
<tr>
<td>44</td>
<td>GND</td>
<td>43</td>
<td>PWM4</td>
</tr>
<tr>
<td>46</td>
<td>USB_DP</td>
<td>45</td>
<td>PWM3</td>
</tr>
<tr>
<td>48</td>
<td>USB_DM</td>
<td>47</td>
<td>CPU_TXD2</td>
</tr>
<tr>
<td>50</td>
<td>GND</td>
<td>49</td>
<td>CPU_RXD2</td>
</tr>
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### Table 2-6. Interposer Interconnect Signal Description

<table>
<thead>
<tr>
<th>Position</th>
<th>Signal</th>
<th>Interconnect Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>CPU_RXD</td>
<td>3V Debug UART RxD or USB AUX_IN</td>
</tr>
<tr>
<td>2</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>3</td>
<td>CPU_TXD</td>
<td>3V Debug UART TxD or USB AUX_OUT</td>
</tr>
<tr>
<td>5</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>8</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>14</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>15</td>
<td>EXT_RSTn</td>
<td>External Reset, Open/GND discrete, of Intel® RealSense™ ID Solution F450 module (should be connected to open drain I/O).</td>
</tr>
<tr>
<td>20</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>21</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>22</td>
<td>GPIO_6</td>
<td>3V General Purpose I/O, defined by FW (No external PU/PD needed).</td>
</tr>
<tr>
<td>26</td>
<td>GND</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td>GND</td>
<td></td>
</tr>
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<td>33</td>
<td>GPIO_4</td>
<td>GPIO Output (No external PU/PD needed).</td>
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<td>35</td>
<td>GND</td>
<td>Ground</td>
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<td>38</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>44</td>
<td>GND</td>
<td></td>
</tr>
<tr>
<td>46</td>
<td>USB2+</td>
<td>USB 2 differential pair positive</td>
</tr>
<tr>
<td>47</td>
<td>CPU_TxD2</td>
<td>3V UART TxD (Output from the Intel® RealSense™ ID Solution F450 Module)</td>
</tr>
<tr>
<td>48</td>
<td>USB2-</td>
<td>USB 2 differential pair negative</td>
</tr>
<tr>
<td>49</td>
<td>CPU_RxD2</td>
<td>3V UART RxD (Input into the Intel® RealSense™ ID Solution F450 Module)</td>
</tr>
<tr>
<td>50</td>
<td>GND</td>
<td>Ground</td>
</tr>
<tr>
<td>51</td>
<td>Vin</td>
<td>F450 Power IN – 3.15V – 5.5V (Peak power consumption = 3.65W)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Typical = 3.6V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Maximum = 5.5V</td>
</tr>
</tbody>
</table>
### 2.2 Optical System

#### 2.2.1 Cameras

The optical system consists of two synced semi-mechanically aligned cameras which capture images from different perspectives for the purpose of 3D triangulation to create disparity. The Intel® RealSense™ ID Solution F450 utilizes the cameras for face authentication. The right camera can provide 1080P RGB.

**Note:** Although face authentication utilizes RAW format from the sensor, only the processed RGB video stream can be transmitted externally to the host.
Table 2-7: Camera Parameters

<table>
<thead>
<tr>
<th>DIMENSION</th>
<th>CAMERA 1</th>
<th>CAMERA 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Position (Camera Perspective)</td>
<td>Left</td>
<td>Right</td>
</tr>
<tr>
<td>Orientation</td>
<td>Portrait</td>
<td>Portrait</td>
</tr>
<tr>
<td>RGB Support</td>
<td>Yes</td>
<td>No</td>
</tr>
</tbody>
</table>

Table 2-8: Lens Parameters

<table>
<thead>
<tr>
<th>DIMENSION</th>
<th>MIN</th>
</tr>
</thead>
<tbody>
<tr>
<td>Field of View (Mono)</td>
<td>HFOV = 59°, VFOV = 80°</td>
</tr>
</tbody>
</table>

2.2.2 IR Illuminators

The optical system consists of an IR LED for face illumination in low or non-uniform lighting conditions and an IR dot projector for 3D triangulation to create disparity. The illumination angle and placement of these components is chosen based on the field of view for both cameras.

The IR illuminators are both tested to the eye safety industry standards as defined by the IEC.

Table 2-9: IR Illuminator Parameters

<table>
<thead>
<tr>
<th>DIMENSION</th>
<th>DOT PROJECTOR</th>
<th>LED</th>
</tr>
</thead>
<tbody>
<tr>
<td>Orientation</td>
<td>Portrait</td>
<td>Portrait</td>
</tr>
<tr>
<td>Wavelength</td>
<td>850nm</td>
<td>850nm</td>
</tr>
<tr>
<td>Regulatory</td>
<td>Class 1 (IEC-60825-1 ED3)</td>
<td>RG0 (IEC-62471)</td>
</tr>
</tbody>
</table>

2.3 Thermal

2.3.1 Intel® RealSense™ ID Solution F450 Thermal

The Intel® RealSense™ ID Solution F450 module is designed to work indoors and outdoors across a wide temperature range.

Table 2-10: Intel® RealSense™ ID Solution F450 Temperature Specifications

<table>
<thead>
<tr>
<th>DIMENSION</th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Storage Temperature (Ambient), Not Powered</td>
<td>-30</td>
<td>-</td>
<td>+70</td>
<td>°C</td>
</tr>
<tr>
<td>Case (Backside PCB) Temperature, Powered</td>
<td>0</td>
<td>-</td>
<td>+60</td>
<td>°C</td>
</tr>
</tbody>
</table>

NOTES:
1. All temperatures are calculated under the assumption of 100% duty-cycle of the device.
2. Specification is based on preliminary assessment and will continue quality and reliability to finalize in later datasheet versions.
3. Powered temperature is average across backside PCB.

The optical properties of the camera and projector components can change based on the temperature of the component. This can range to a decrease in camera quality or decrease in the illumination power. Either of these can have an impact on face authentication performance, including the user being required to stand closer to the Intel® RealSense™ ID Solution F450 module.

Table 2-11: Temperature Component Considerations

<table>
<thead>
<tr>
<th>COMPONENT</th>
<th>PARAMETER</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>Projector</td>
<td>Optical Power</td>
<td>The distance in which can be detected by the camera.</td>
</tr>
<tr>
<td></td>
<td>Field of View</td>
<td>The area in which covers the camera FOV.</td>
</tr>
<tr>
<td></td>
<td>Wavelength</td>
<td>Aligned to sensitivity of the camera IR channel.</td>
</tr>
<tr>
<td>Camera</td>
<td>Field of View</td>
<td>The area in which covers the camera FOV.</td>
</tr>
<tr>
<td></td>
<td>MTF</td>
<td>Sharpness of fuzziness of the camera FOV.</td>
</tr>
<tr>
<td></td>
<td>Sensitivity</td>
<td>Detection of user wavelengths across the FA spectrum.</td>
</tr>
<tr>
<td></td>
<td>SNR</td>
<td>Signal quality of the input data into the FA algorithms.</td>
</tr>
<tr>
<td></td>
<td>Lag</td>
<td>Time delta in which photons are measured by the camera.</td>
</tr>
</tbody>
</table>

Table 2-12: Intel® RealSense™ ID Solution F450 – Component Power

<table>
<thead>
<tr>
<th>Component</th>
<th>Power</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Left Imager</td>
<td>90</td>
<td>mW</td>
</tr>
<tr>
<td>Right Imager</td>
<td>90</td>
<td>mW</td>
</tr>
<tr>
<td>Projector</td>
<td>61</td>
<td>mW</td>
</tr>
<tr>
<td>SOC + Flash</td>
<td>703</td>
<td>mW</td>
</tr>
<tr>
<td>LED</td>
<td>356</td>
<td>mW</td>
</tr>
</tbody>
</table>

2.3.2 Intel® RealSense™ ID Solution F455 Thermal

Table 2-13: Intel® RealSense™ ID Solution F455 Temperature Specifications

<table>
<thead>
<tr>
<th>DIMENSION</th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Storage Temperature (Ambient), Not Powered</td>
<td>-30</td>
<td>-</td>
<td>+70</td>
<td>°C</td>
</tr>
<tr>
<td>Backside Case Temperature, Powered&lt;sup&gt;2&lt;/sup&gt;</td>
<td>0</td>
<td>-</td>
<td>+55</td>
<td>°C</td>
</tr>
</tbody>
</table>

**NOTES:**
1. All temperatures are calculated under the assumption of 100% duty-cycle of the device.
2. Specification is based on preliminary assessment and will continue quality and reliability to finalize in later datasheet versions.
2.3.3 Power Profiling

To minimize thermal dissipation design requirements for integrating the module into a system, it is recommended to maintain a 1.5s cycle mode of work. Face authentication is designed to work within this time span, but if the module is operating continuously this will cause heating which may not be dissipated by design.

2.4 Manual Software Update

The Intel® RealSense™ ID Solution F450 implements 512Mb of local storage which will contain the OS and face authentication runtime. Intel will provide a FW image which is used for initial provisioning of the Intel® RealSense™ ID Solution F450. The FW image contains a local flash update runtime which allows the host to push new FW payloads to update the Intel® RealSense™ ID Solution F450 device.

The Intel® RealSense™ ID SDK offers an API for verifying the presence of an updated SDK version. Users can seamlessly incorporate this API into their host applications to perform manual or recurring checks for any new SDK releases. Upon detecting a new version, users have the flexibility to decide whether they wish to update their host SDK. To initiate the update process, users are required to manually retrieve the pertinent version from the open-source repository.

2.5 Manual Firmware Upgrade

The Intel® RealSense™ ID SDK offers multiple APIs for facilitating a manual firmware upgrade. One of these APIs allows users to check for the availability of a new firmware (FW) version. Once a new version is identified, the user can decide whether to proceed with the FW upgrade. The latest FW version is accessible through our open-source releases.

Manual Download and Upgrade:

- Download the FW binary manually from our open-source releases.
- Utilize the API to initiate the FW upgrade by specifying the location of the downloaded binary.
- In case the downloaded FW is incompatible with the current SDK version, an error message will be generated.

Table 2-14: Firmware Upgrade Parameter

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SUPPORT</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>FW Public Key</td>
<td>Yes</td>
<td>Stored on the Intel® RealSense™ ID Solution F450 device.</td>
</tr>
<tr>
<td>Signed FW</td>
<td>Yes</td>
<td>Update tool will compare signature before update.</td>
</tr>
<tr>
<td>Rollback Protection</td>
<td>No</td>
<td>Host system checks version before initiating an update.</td>
</tr>
<tr>
<td>Local Update</td>
<td>No</td>
<td>Host system must push payload.</td>
</tr>
<tr>
<td>Update Tool</td>
<td>Yes</td>
<td>Local Storage via Intel Tool</td>
</tr>
<tr>
<td>Host Interface</td>
<td>Yes</td>
<td>USB and UART</td>
</tr>
</tbody>
</table>
The small form factor of the Intel® RealSense™ ID Solution F450 module enables integration flexibility. The host system will mount and tilt the Intel® RealSense™ ID Solution F450 module based on the system installation height specification, user height range, and user approach paths to the device. The placement and orientation of the system design will depend on the following factors.

1. Sensor and lens field of view.
2. User height range and approach path.
3. Detection range of the face authentication system.

### 3.1 Mechanical Dimensions

**Table 3-1: Intel® RealSense™ ID Solution F450 Module Dimensions**

<table>
<thead>
<tr>
<th>DIMENSION</th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>TOLERANCE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>X</td>
<td>49.70</td>
<td>49.80</td>
<td>49.90</td>
<td>± 0.10</td>
<td>mm</td>
</tr>
<tr>
<td>Y</td>
<td>17.90</td>
<td>18</td>
<td>18.10</td>
<td>± 0.10</td>
<td>mm</td>
</tr>
<tr>
<td>Z</td>
<td>4.23</td>
<td>4.38</td>
<td>4.53</td>
<td>± 0.15</td>
<td>mm</td>
</tr>
</tbody>
</table>

**Figure 3-1: Intel® RealSense™ ID Solution F450 Module Dimensions**

**Table 3-2: Intel® RealSense™ ID Solution F455 Peripheral Dimensions**

<table>
<thead>
<tr>
<th>DIMENSION</th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>TOLERANCE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>X</td>
<td>61.90</td>
<td>62</td>
<td>62.10</td>
<td>± 0.10</td>
<td>mm</td>
</tr>
<tr>
<td>Y</td>
<td>32.40</td>
<td>32.50</td>
<td>32.60</td>
<td>± 0.10</td>
<td>mm</td>
</tr>
<tr>
<td>Z</td>
<td>10.85</td>
<td>11</td>
<td>11.15</td>
<td>± 0.15</td>
<td>mm</td>
</tr>
</tbody>
</table>
3.2 Cover Glass

The Intel® RealSense™ ID Solution F450 module components must be covered to minimize impact of dust and humidity. The transparent cover glass material stack-up must provide acceptable transmission based on the component wavelengths. The through hole sizes of the cover glass is dependent on the optical component field of view or field of illumination as well as the cone diameter from the optical component to the cover glass.

A 3D CAD assessment should be completed to ensure no collision between the cone part of each optical component and the cover glass opening.

Table 3-3: Transmissivity Requirement

<table>
<thead>
<tr>
<th>THROUGH HOLE</th>
<th>TRANSMISSIVITY</th>
</tr>
</thead>
<tbody>
<tr>
<td>1, 2, 3 and 4</td>
<td>&gt;98% @ 400nm to 880nm</td>
</tr>
</tbody>
</table>

3.3 Optical Isolation

It is recommended to isolate the cameras from the illumination components to prevent reflections off the cover material. Not properly isolating the cameras can result in leakage light from the illumination components. It is recommended to use a gasket material in between the cover glass and the Intel® RealSense™ ID Solution F450 module. The gasket material needs to be compliant so that it does not transmit chassis flex forces to the module.
3.4 Orientation

The module can be assembled in either 0° or 180° landscape rotation. The module cannot be assembled in a 90° or 270° portrait orientation.

3.5 System Tilt

Given the field of view of the cameras and illumination components, assembling the Intel® RealSense™ ID Solution F450 module parallel to a typical mounting surface may not cover the intended user height range for the product requirement. To mitigate, the Intel® RealSense™ ID Solution F450 module can be assembled at an angle to adjust the min and max user height for face authentication.

<table>
<thead>
<tr>
<th>DIMENSION</th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Vertical Range @ 55cm</td>
<td>120</td>
<td>-</td>
<td>190</td>
<td>cm</td>
</tr>
<tr>
<td>System Tilt</td>
<td>11</td>
<td>16</td>
<td>21</td>
<td>deg</td>
</tr>
<tr>
<td>Height Placement(1)</td>
<td>1.1</td>
<td>1.2</td>
<td>1.25</td>
<td>m</td>
</tr>
</tbody>
</table>

NOTES:
(1) The height placement is with respect to the sensor center line position.
3.5.1 Heat Sink

Although the peak power is up to 3W, the Intel® RealSense™ ID Solution F450 module is only expected to dissipate a total of 1.3W while the optical system is activated during face authentication. This assumes a 30% duty cycle while the illumination components are active.

If needed the system design will need to include a heat sink to maintain proper operating temperatures for the Intel® RealSense™ ID Solution F450 components. Unless multiple retries are required, a typical face authentication session can continue for up to 2s.
4 **Software Definition**

The Intel® RealSense™ ID Solution F450 module executes in a closed system running a real time operating system. The face authentication uses image pre-processing, anti-spoofing checking, and the SOC matrix acceleration engine embedded to extract the face features of the user. These face features are then stored during enrollment of a new user or compared during authentication to determine whether the user is valid.

### 4.1 Intel® RealSense™ ID Software Licensing

The Intel® RealSense™ ID Anti-Spoofing and Facial Authentication Software require an active subscription to be functional. These subscriptions are separately orderable from the peripheral and module hardware. Software subscriptions can be ordered from the Intel RealSense™ web store or your preferred distributor.

### 4.2 Intel® RealSense™ ID SDK for Facial Authentication

The Intel® RealSense™ ID SDK for Facial Authentication is a library providing Facial Authentication capabilities to applications using the Intel® RealSense™ ID Solution F450 or F455. It is open source and available on [https://github.com/IntelRealSense/RealSenseID](https://github.com/IntelRealSense/RealSenseID).

### 4.3 Face Authentication

The Intel® RealSense™ ID Solution F450 optical system and the face authentication algorithms are optimized for functional and performance profiles targeted at access control devices. In this use case, one or more users will walk up to the device in the runtime or standby state, the host will notify the Intel® RealSense™ ID Solution F450 module to perform face authentication and the Intel® RealSense™ ID Solution F450 module will respond back with the result to the host software.

<table>
<thead>
<tr>
<th>METRIC</th>
<th>VALUE</th>
<th>METRIC</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of Users</td>
<td>500</td>
<td>Users</td>
</tr>
<tr>
<td>Authentication Range</td>
<td>30 to 100 cm</td>
<td>cm</td>
</tr>
<tr>
<td>Anti-Spoofing Range</td>
<td>30 to 100 cm</td>
<td>cm</td>
</tr>
<tr>
<td>Ambient Light</td>
<td>0 to 50k* lux</td>
<td>lux</td>
</tr>
</tbody>
</table>

(*) Reduced performance may be exhibited in semi-shade or direct sunlight.
### Table 4-2: Face Authentication Metrics

<table>
<thead>
<tr>
<th>METRIC</th>
<th>DESCRIPTION</th>
<th>VALUE</th>
</tr>
</thead>
<tbody>
<tr>
<td>False Acceptance Rate (FAR)</td>
<td>Recognize someone else as you.</td>
<td>1:1M</td>
</tr>
<tr>
<td>True Acceptance Rate (TAR)</td>
<td>Recognize you as you.</td>
<td>99.76%</td>
</tr>
<tr>
<td>Spoof Acceptance Rate (SAR)</td>
<td>Recognizes a recorded sample of you as you (photo or model).</td>
<td>&lt;0.1%</td>
</tr>
</tbody>
</table>

### Table 4-3: Performance Metrics

<table>
<thead>
<tr>
<th>METRIC</th>
<th>DESCRIPTION</th>
<th>VALUE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cold Latency</td>
<td>Cold Power Up to Face Authentication Result</td>
<td>&lt;3s</td>
</tr>
<tr>
<td>Warm Latency</td>
<td>Wake Up to Face Authentication Result</td>
<td>&lt;1.5s</td>
</tr>
<tr>
<td>FA Flow</td>
<td>Time to Complete Face Authentication</td>
<td>&lt;0.8s</td>
</tr>
</tbody>
</table>
5  System Integration

The system design and topology will be owned by the host system OEM with the Intel® RealSense™ ID Solution F450 module as a subassembly component. There are product assembly recommendations that Intel provides to ensure proper function of the Intel® RealSense™ ID Solution F450 module, but design parameters such as user sensing and user feedback will be defined by the OEM.

Figure 5-1: System Design Considerations

5.1 Product Assembly

The Intel® RealSense™ ID Solution F450 module is intended to be assembled into the system in a horizontal or landscape orientation. The Intel® RealSense™ ID Solution F450 module should be secured to ensure the camera and illumination extrinsic specifications are not exceeded for post assembly and throughout the thermal operating range of the product.

Table 5-1: Product Assembly Recommendation

<table>
<thead>
<tr>
<th>NAME</th>
<th>DIMENSION</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>YAW (Camera to Camera)</td>
<td>1</td>
<td>deg</td>
</tr>
<tr>
<td>PITCH (Camera to Camera)</td>
<td>1</td>
<td>deg</td>
</tr>
<tr>
<td>ROLL (Camera to Camera)</td>
<td>1</td>
<td>deg</td>
</tr>
<tr>
<td>YAW (Camera to Illumination)</td>
<td>1</td>
<td>deg</td>
</tr>
<tr>
<td>PITCH (Camera to Illumination)</td>
<td>1</td>
<td>deg</td>
</tr>
<tr>
<td>ROLL (Camera to Illumination)</td>
<td>1</td>
<td>deg</td>
</tr>
</tbody>
</table>

The product design and assembly recommendations for the Intel® RealSense™ ID Solution F450 module are dependent on optimizing the optical performance by minimizing moisture and dust impacts.
Table 5-2: Product Design Recommendation

<table>
<thead>
<tr>
<th>NAME</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ingress Protection</td>
<td>The system integrator will define ingress protection level based on the installation location of the product (e.g. indoor/outdoor). The Intel® RealSense™ ID Solution F450 optical elements should be protected from dust and moisture accumulation.</td>
</tr>
<tr>
<td>Anti-Fog Coating</td>
<td>If this is a design consideration based on the installation location of the product, a coating should be applied to the glass to prevent fogging from occurring. Initial testing has shown that a standard glass coating (e.g. DuPont de-icing spray) proved insufficient</td>
</tr>
<tr>
<td>Enclosure</td>
<td>Based on installation location of the product (e.g. indoor/outdoor), recommend designing metal enclosure and not use plastic materials that might allow humidity to pass through.</td>
</tr>
<tr>
<td>Purging</td>
<td>Guarantee below 5% humidity during assembly.</td>
</tr>
<tr>
<td>Pressurizing*</td>
<td>0.5psig with N2 (nitrogen) so no humidity will be able to enter the system.</td>
</tr>
<tr>
<td>Desiccant*</td>
<td>Replaceable bag/capsule - Molecular Sieve (ZEOLITE) or Activated Alumina.</td>
</tr>
</tbody>
</table>

* Might not be required due to the small dimensions of the enclosure, but nice to have to provide best protection against humidity.

5.1.1 Optical Assembly Considerations

The Intel® RealSense™ ID Solution F450 module will be shipped with an easily removable protective film on the camera and illumination components. This must be removed before assembly as the Intel® RealSense™ ID Solution F450 module will not function with this film installed. It is recommended that the cover glass has similar film protection to minimize dust and fingerprints.

To ensure dust and particles are minimized on the lens and cover glass during assembly it is recommended that the assembly room maintain a clean room metric of greater than 10,000 PPM. If an operator must manually handle the components, then gloves and mask is recommended. The following steps should then be followed to during assembly and performance validation.

1. Remove the protective film on the Intel® RealSense™ ID Solution F450 module optical components.
2. Remove the protective film on the cover glass.
3. Remove the liner on the cover glass edge that protects the glue.
4. Assemble the Intel® RealSense™ ID Solution F450 module and the cover glass.
5. Validate the device.
6. Clean the cover glass, then add protective film.

5.2 Trigger Heuristic

The Intel® RealSense™ ID Solution F450 module does not include a system for sensing user approach, but does support power management for runtime, standby, and wake. To improve the power efficiency of the Intel® RealSense™ ID Solution F450 system, it is recommended that the host system implement either motion or proximity sensing capabilities to determine when to wake and start a Face Authentication session. The host system would receive an interrupt from the user approach sensors.
and then propagate that request to the Intel® RealSense™ ID Solution F450 module interface which will cause a wake event.

Table 5-3: User Sensing Parameters

<table>
<thead>
<tr>
<th>DESIGN PARAMETERS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Horizontal Field of View and Zone Sectioning</td>
</tr>
<tr>
<td>Vertical Field of View and Zone Sectioning</td>
</tr>
<tr>
<td>User Path (Walk-Up versus Walk-By)</td>
</tr>
<tr>
<td>Sensitivity Threshold Granularity and Control</td>
</tr>
<tr>
<td>Sensing Distance</td>
</tr>
</tbody>
</table>

Figure 5-2: Trigger Heuristic

5.3 User Feedback

If the system design requires both audio and visual feedback the host will need to interface to both speaker and/or display/LED components. This will provide the necessary user feedback during enrollment and authentication scenarios. It is up to the host system to define each audio and visual prompt.

As the enrollment and authentication flows are initiated by the host system, the Intel® RealSense™ ID Solution F450 module can return status codes. It is up to the host system to translate those codes into respective audio or visual feedback which can cause the user to perform certain actions.

Table 5-4: Result Scenario Feedback

<table>
<thead>
<tr>
<th>SCENARIO</th>
<th>AUDIO</th>
<th>VISUAL</th>
</tr>
</thead>
<tbody>
<tr>
<td>Authentication Success</td>
<td>“Welcome”</td>
<td>GREEN (Blink)</td>
</tr>
<tr>
<td>Scenario</td>
<td>Audio</td>
<td>Visual</td>
</tr>
<tr>
<td>--------------------------------</td>
<td>------------------</td>
<td>-----------------</td>
</tr>
<tr>
<td>No Face Detected</td>
<td>“Not Detected”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face is Too Far Top</td>
<td>“Lower Head”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face is Too Far Bottom</td>
<td>“Raise Head”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face is Too Far Right</td>
<td>“Move Left”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face is Too Far Left</td>
<td>“Move Right”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face Tilt is Too Up</td>
<td>“Tilt Head Down”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face Tilt is Too Down</td>
<td>“Tilt Head Up”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face Tilt is Too Right</td>
<td>“Tilt Head Left”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face Tilt is Too Left</td>
<td>“Tilt Head Right”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face is Not Frontal</td>
<td>“Face Front”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face is too Far from Camera</td>
<td>“Move Forward”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Face is too Close to the Camera</td>
<td>“Move Backward”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Multiple Faces Detected</td>
<td>“Too Many Faces”</td>
<td>YELLOW (BLINK)</td>
</tr>
<tr>
<td>Device Error</td>
<td>“Error”</td>
<td>RED (SOLID)</td>
</tr>
</tbody>
</table>
6 Regulatory Compliance

6.1 System Laser Compliance

The Intel® RealSense™ ID Solution series certification is transferable to the system and no system recertification is required. However, the following statements and labels must be included in the user manual of the end product.

6.1.1 Certification Statement

This product is classified as a Class 1 Laser Product under the EN/IEC 60825-1, Edition 3 (2014) internationally.

This product complies with FDA performance standards for laser products except for conformance with IEC 60825-1 Ed. 3 as described in Laser Notice No. 56, dated May 8, 2019.

6.1.2 Explanatory Label

![CLASS 1 LASER PRODUCT EN/IEC 60825-1, 2014]

This device complies with FDA performance standards for laser products except for conformance with IEC 60825-1 Ed. 3, as described in Laser Notice No. 56, dated May 8, 2019.

6.1.3 Cautionary Statements

⚠️ System integrators should refer to their respective regulatory and compliance owner to finalize regulatory requirements for a specific geography.

⚠️ **Caution** - Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.
- Do not power on the product if any external damage was observed.
- Do not attempt to open any portion of this laser product. There are no user serviceable parts.
- Invisible laser radiation when opened. Avoid direct exposure to beam.
- There are no service/maintenance, modification, or disassembly procedures for the stereo module and infrared projector. The system integrator must either notify Intel or return modules before any failure analysis is performed.
- Modification or service of the hardware, specifically the infrared projector, may cause the emissions to exceed Class 1.
- Do not try to update Intel RealSense ID Solution F455 firmware that is not officially released for Intel RealSense ID Solution F455 SKU and revision.

6.1.4 US FDA Accession Number

Table 6-1. U.S. FDA Accession Number

<table>
<thead>
<tr>
<th>Component</th>
<th>U.S. FDA accession numbers</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel® RealSense™ ID Solution F455/F450</td>
<td>2010525</td>
</tr>
</tbody>
</table>

This accession number should be entered into Box B.1 of the Food and Drug Administration (FDA) 2877 Declaration for Imported Electronic Products Subject to Radiation Control Standards.

6.2 Regulatory Compliance

6.2.1 Manufacturer’s Information

Intel Corporation:
Attn: Corp. Quality
2200 Mission College Blvd,
Santa Clara, CA 95054-1549, USA

6.2.2 EU Single Place of Contact

Attn: Corp Quality
Intel Deutschland GmbH
Am Campeon 10-12
Neubiberg, 85579 – Germany

6.2.3 UK Single Place of Contact

Intel Corporation (UK) Ltd.
Attn: Corp Quality
Pipers Way, Swindon
Wiltshire SN3 1RJ, UK
6.2.4 **FCC Part 15 Declaration of Conformity**

Intel® RealSense™ ID Solution F455

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Intel Corporation: Attn: Corp. Quality, 2200 Mission College Blvd, Santa Clara, CA 95054-1549, USA

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

Changes or modifications not expressly approved by the manufacturer could void the user’s authority to operate the equipment.

CAUTION: To comply with the limits of the Class B digital device, pursuant to Part 15 of the FCC Rules, this device must be installed with computer equipment certified to comply with Class B limits. All cables used to connect to the computer and peripherals must be shielded and grounded. Operation with non-certified computers or non-shielded cables may result in interference to radio or television reception.

6.2.5 **Canada**

CAN ICES-3 (B)/NMB-3(B)

This digital apparatus does not exceed the Class B limits for radio noise emissions from digital apparatus set out in the Interference-Causing Equipment Standard ICES-003.

Le présent appareil numerique német pas de bruits radio électriques dépassant les limites applicables aux appareils numériques de la classe B prescrites dans la présente norme sur le matériel brouilleur, NMB-003.

6.2.6 **European Directives**

This product complies with all applicable European directives.

CAUTION: To keep the product in compliance with the European CE requirement, the device must be installed with CE certified computer equipment which meets the Class B limits. Operation with non-
certified computers or incorrect cables may result in interference to other devices or undesired effect to the product.

6.2.7 Australia

Due to the very small size of the product, the marking has been placed in this datasheet.

6.2.8 Japan

この装置は、クラスB機器です。この装置は、住宅環境で使用することを目的としていますが、この装置がラジオやテレビジョン受信機に近接して使用されると、受信障害を引き起こすことがあります。取扱説明書に従って正しい取り扱いをして下さい。VCCI-B

This device is a Class B information technology device. This device is intended for use in a home environment, but if this device is used close to a radio or television receiver it may cause reception interference. Please handle according to the instruction manual correctly. VCCI-B

6.2.9 South Korea

인증 번호: R-R-CPU-F455C
제조사: Intel Corporation
상품명: Intel® RealSense™ ID Solution
모델: F455
제조국: 태국

인증 번호: R-R-CPU-F450
제조사: Intel Corporation
상품명: Intel® RealSense™ ID Solution
모델: F450
제조국: 중국

6.2.10 Customs Union

Наименование продукции: Intel® RealSense™ ID Solution
Модель: F455
Производитель: Intel Corporation
Сделано в Таиланде
## 6.3 Ecology Compliance

### 6.3.1 China RoHS Declaration

#### China RoHS Declaration

**Hazardous Substances Table**

<table>
<thead>
<tr>
<th>Component Name</th>
<th>Hazardous Substance</th>
<th>Pb</th>
<th>Hg</th>
<th>Cd</th>
<th>Cr (VI)</th>
<th>PBB</th>
<th>PBDE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Solution</td>
<td></td>
<td>X</td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
</tr>
<tr>
<td>Printed Board Assemblies</td>
<td></td>
<td>X</td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
</tr>
<tr>
<td>Tripod</td>
<td></td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
</tr>
<tr>
<td>Cable</td>
<td></td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
<td>○</td>
</tr>
</tbody>
</table>

○: 表示该有毒有害物质在该部件所有均质材料中的含量均在GB/T 26572标准规定的限量要求以下。

○: Indicates that this hazardous substance contained in all homogeneous materials of such component is within the limits specified in GB/T 26572.

×: 表示该有毒有害物质至少在该部件的某一均质材料中的含量超出GB/T 26572标准规定的限量要求。

×: Indicates that the content of such hazardous substance in at least a homogeneous material of such component exceeds the limits specified in GB/T 26572.

对销售之日的所售产品,本表显示我公司供应链的电子信息产品可能包含这些物质。注意: 在所售产品中可能会也可能不会含有所有所列的部件。

This table shows where these substances may be found in the supply chain of our electronic information products, as of the date of sale of the enclosed product. Note that some of the component types listed above may or may not be a part of the enclosed product.

除非另外特别的标注,此标志为针对所涉及产品的环保使用期限标志。某些可更换的零部件可能会有一个不同的环保使用期限（例如,电池单元模块）。

此环保使用期限只适用于产品在产品手册中所规定的条件下工作。

The Environment-Friendly Use Period (EFUP) for all enclosed products and their parts are per the symbol shown here, unless otherwise marked. Certain field-replaceable parts may have a different EFUP (for example, battery modules) number. The Environment-Friendly Use Period is valid only when the product is operated under the conditions defined in the product manual.
6.3.2 Waste Electrical and Electronic Equipment (WEEE)

“In the EU, this symbol means that this product must not be disposed of with household waste. It is your responsibility to bring it to a designated collection point for the recycling of waste electrical and electronic equipment. For more information, contact the local waste collection center or your point of purchase of this product.”

6.3.3 Turkey

Türkiye Cumhuriyeti: EEE Yönetmeliğine Uygundur

§§
Figure 7-1. Intel® RealSense™ ID Solution F455

Note: Camera masking lens removed for clarity
Figure 7-2. Intel® RealSense™ ID Solution F450

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN MILLIMETERS.
TOLERANCES:
All Untolerrated Linear Dimensions: ±0.2
Angles: ±0.5°
Appendix A – Interposer Reference Designs

Figure 8-1. USB Interposer Reference Design
Figure 8-2. UART Interposer Reference Design

Customer I/O connector (should be modified according to customer connector and pinout)

<table>
<thead>
<tr>
<th>Pin #</th>
<th>Name</th>
<th>Voltage level</th>
<th>FNB n/D</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>SIG1</td>
<td>5V</td>
<td>In/Out</td>
<td>Input to the UART module (input to the FNB module)</td>
</tr>
<tr>
<td>2</td>
<td>SIG2</td>
<td>5V</td>
<td>In/Out</td>
<td>Input to the UART module (output from the FNB module)</td>
</tr>
<tr>
<td>3</td>
<td>SIG3</td>
<td>5V</td>
<td>In</td>
<td>External Reset, CTS/DSR/BUS, output from the FNB module. Should be connected to open drain (30k)</td>
</tr>
<tr>
<td>4</td>
<td>SIG4</td>
<td>5V</td>
<td>Out</td>
<td>General purpose I/O, defined by FNB. No need external pull-up (30k)</td>
</tr>
<tr>
<td>5</td>
<td>SIG5</td>
<td>5V</td>
<td>Out</td>
<td>SPI output. No need external pull-up (30k)</td>
</tr>
<tr>
<td>6</td>
<td>SIG6</td>
<td>5V</td>
<td>Out</td>
<td>UART TxD (output from the FNB module)</td>
</tr>
<tr>
<td>7</td>
<td>SIG7</td>
<td>5V</td>
<td>Out</td>
<td>UART RxD, input from the FNB module</td>
</tr>
<tr>
<td>8</td>
<td>SIG8</td>
<td>5V</td>
<td>Out</td>
<td>Power pin [3.3V] Minimum: 3.0V Maximum: 3.6V Typical: 3.3V</td>
</tr>
<tr>
<td>9</td>
<td>SIG9</td>
<td>5V</td>
<td>Out</td>
<td>Power pin [3.3V] Minimum: 3.0V Maximum: 3.6V Typical: 3.3V</td>
</tr>
</tbody>
</table>